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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>			Application Number	10/660,565-Conf. #5308	
			Filing Date	September 12, 2003	
			First Named Inventor	Jeff A. Mckee	
			Art Unit	2811	
			Examiner Name	Andrew O. Arena	
Sheet	1	of	1	Attorney Docket Number	M4065.0570/P570-A

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>3</sup>
		Country Code <sup>4</sup> -Number <sup>5</sup> -Kind Code <sup>6</sup> (if known)				

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NON PATENT LITERATURE DOCUMENTS				
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/A.O.A./	CA	H. Watanabe et al., "Hemispherical Grained Si Formation on <i>in-situ</i> Phosphorus Doped Amorphous-Si Electrode for 256Mb DRAM's Capacitor", IEEE Transactions on Electron Devices, Vol. 42, No. 7, July 1995.		

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Examiner Signature	/Andrew O. Arena/	Date Considered	02/18/2009
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